

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
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	B	US-5,925,099 A	07-1999	Futral et al.	709/204
	C	US-5,333,269 A	07-1994	Calvignac et al.	709/215
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	IBM Technical Disclosure Bulletin, Vol.36, No. 02, Feb. 1993, pages 67-69, "Buffer Management Scheme for Gigabit IP Routers"
*	V	IBM Technical Disclosure Bulletin, Vol. 34, No. 10A, March 1992, pages 464-465, "Multicast/Broadcast Mechanism for a Shared Buffer Packet Switch"
*	W	IBM Technical Disclosure Bulletin, Vol. 37, No. 11, Nov. 1994, pages 641-645, "Hybrid Switch Control Scheme for Fast Point-to-Point/Multicast/Hunt-Group Connection Setup"
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.